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L1: (181118) disk adj1 drive L2: (42844) clock near5 delay L3: (1169380) recording L4: (936) pre-compensate or pre-compensation L5: (883) precompensate or precompensation L6: (1690) 4 or 5 L7: (0) 1 same 2 same 3 same 6 L8: (65) 1 and 2 and 3 and 6 L9: Failed												
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1	2	3	4	5	6	7	8	9	10	11	12	13
Document ID	Issue Date	Pages	Title	Current GN	Current XRef	Retrieval C	Inventor	S	C	P	T	U
US 20030189778 A1	20031009	14	Disk drive writer waveform induced precompensation	360/29	360/46; 360/66		Elliott, Carl F. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 20020176299 A1	20021128		High-speed zero phase restart of a multiphase	365/221	365/233		Ruegg, Michael A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 20020176194 A1	20021128		High-speed programmable synchronous counter for use	360/51			Ruegg, Michael A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 20020176188 A1	20021128		Offset cancellation of charge pump based phase	360/46			Ruegg, Michael A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 20020176186 A1	20021128		Efficient analog front end for a read/write channel of	360/32	360/51		Cyrusian, Sasan et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 20020175724 A1	20021128		Method and system for managing a pulse width of a	327/172			Cyrusian, Sasan et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 20020159176 A1	20021031		Look ahead write precompensation	360/46	360/51		Elliott, Carl F. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 20020154430 A1	20021024		Read/write channel	360/25			Rae, James W. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 20020126604 A1	20020912		Write compensation for data storage and communication	369/47.53	369/59.11		Powelson, Judith C. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 20020015247 A1	20020207		HIGH RESOLUTION WIDE RANGE WRITE PRECOMPENSATION	360/45	360/68		ROSKY, DAVID S. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 6785071 B2	20040831		Disk drive writer waveform induced precompensation	360/29	360/46; 360/66		Elliott, Carl F. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 6738205 B1	20040518		Self-writing of servo patterns in disk drives	360/17	360/75		Moran, Patrick et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 6700722 B2	20040302		High-speed zero phase restart of a multiphase	360/51	327/152; 331/21		Ruegg, Michael A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 6690525 B2	20040210		High-speed programmable synchronous counter for use	360/51	331/25; 375/376		Ruegg, Michael A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 6662303 B1	20031209		Write precompensation circuit and read channel	713/400	711/167; 713/500		Toosky, Zabih et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 6661590 B2	20031209		Efficient analog front end for a read/write channel of	360/32	360/51; 360/66		Cyrusian, Sasan et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 6653879 B2	20031125		Method and system for managing a pulse width of a	327/172	327/173; 327/174		Cyrusian, Sasan et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 6621648 B2	20030916		Look ahead write precompensation	360/45	360/46; 360/51		Elliott, Carl F. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
US 6594094 B2	20030715		Read/write channel	360/25	360/46		Rae, James W. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
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Drafts										
		Document ID	Issue Date	Pages	Title	Current Doc	Current Edx	Retrieval	Inventor	
20	P	US 6438703 B1	20020820		Method and system for selectively varying signal	713/401	713/503		Berning, Lynn Charles et al.	
21	P	US 6404572 B1	20020611		Circuit and method for generating a write	360/45	360/51; 360/76		Hong, Ghy-Boong	
22	P	US 6337778 B1	20020108		Disk drive employing vector addition of primary phase	360/51	360/46; 360/68		Gagne, Jean-Sebastien	
23	P	US 6243031 B1	20010605		Selectable delay circuit having immunity to	341/68	327/157		Jusuf, Gani et al.	
24	P	US 6219192 B1	20010417		Method and apparatus for the compensation of an additive	360/25	360/46; 360/53		Gopalaswamy, Srinivasan et al.	
25	P	US 6208184 B1	20010327		Method and device for delaying selected	327/161	327/263		Demicheli, Marco et al.	
26	P	US 6191906 B1	20010220		Method for estimating sampling phase from	360/51	360/46		Buch, Bruce D.	
27	P	US 6133861 A	20001017		Selectable delay circuit having immunity to	341/68	327/157		Jusuf, Gani et al.	
28	P	US 6108153 A	20000822		Servo demodulator and method for synchronous servo	360/51	360/77.08		Glover, Kerry C.	
29	P	US 6043944 A	20000328		Method and apparatus for fail-safing and extending	360/51			Yamasaki, Richard G. et al.	
30	P	US 6009534 A	19991228		Fractional phase interpolation of ring	713/503	331/107R		Chiu, Kar-Shing et al.	
31	P	US 5986830 A	19991116		Read/write channel write precompensation system and	360/45	360/51		Hoin, Jerrell F.	
32	P	US 5808822 A	19990915		Apparatus and method for correcting a read signal in	360/65	360/51		Kurachi, Koji et al.	
33	P	US 5734680 A	19980331		Analog implementation of a partial response maximum	375/263	375/262; 375/341		Moore, Charles S. et al.	
34	P	US 5598364 A	19970128		All-MOS precision differential delay line with	365/73	360/51; 365/189.05		McCall, Kevin J. et al.	
35	P	US 5592515 A	19970107		Fully digital data separator and frequency multiplier	375/340	327/156; 327/160		Saban, Rami et al.	
36	P	US 5583705 A	19961210		Write precompensation optimization in a PRML	360/45	360/46		Ziperovich, Pablo A. et al.	
37	P	US 5559645 A	19960924		Disk recording apparatus with adaptive window	360/51	360/53		Miyazawa, Shyoichi et al.	
38	P	US 5553100 A	19960903		Fully digital data separator and frequency multiplier	375/340	327/156; 327/160		Saban, Rami et al.	
39	P	US 5552942 A	19960903		Zero phase start optimization using mean	360/51	375/369; 375/376		Ziperovich, Pablo A. et al.	
40	P	US 5543975 A	19960806		Removal of precompensation in a write data signal from	360/45	360/46		Curran, Daniel J.	
41	P	US 5517146 A	19960514		Write precompensation circuit for compensating	327/276	327/261; 360/46		Yamasaki, Richard	
42	P	US 5493454 A	19960220		Write precompensation optimization in a PRML	360/45	360/46; 360/53		Ziperovich, Pablo A. et al.	
43	P	US 5459757 A	19951017		Tuning and gain control circuit for a PRML read	375/376	360/46; 360/51		Minuhin, Vadim B. et al.	
44	P	US 5427260 A	19950606		Disk drive method using	360/46	360/51		Akhavan, William L. et al.	

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		Document ID	Issue Date	Pages	Title	Current OK	Current XRef	Retrieval	Inventor	
42	P	US 5493454 A	19960220		Write precompensation optimization in a PRML	360/45	360/46; 360/53		Ziperovich, Pablo A. et al.	
43	P	US 5459757 A	19951017		Timing and gain control circuit for a PRML read	375/376	360/46; 360/51		Minuhin, Vadim B. et al.	
44	P	US 5422760 A	19950606		Disk drive method using zoned data recording and	360/46	360/51; 360/55		Abbott, William L. et al.	
45	P	US 5402274 A	19950328		Data separator and signal processing circuit with an	360/51	360/53		Miyazawa, Shyoichi et al.	
46	P	US 5381359 A	19950110		Adaptation and training of digital finite impulse	708/322	360/40		Abbott, William L. et al.	
47	P	US 5375145 A	19941220		Multi-mode gain control loop for PRML class IV sampling	375/345	375/290; 455/234.1		Abbott, William L. et al.	
48	P	US 5369376 A	19941129		Programmable phase locked loop circuit and method of	331/8	331/16; 331/17		Leblebicioglu, Hakan	
49	P	US 5345342 A	19940906		Disk drive using PRML synchronous sampling data	360/48	360/77.0B		Abbott, William L. et al.	
50	P	US 5341387 A	19940823		Viterbi detector having adjustable detection	714/788	375/341; 375/348		Nguyen, Hung C.	
51	P	US 5341249 A	19940823		Disk drive using PRML class IV sampling data detection	360/46	360/39; 360/51		Abbott, William L. et al.	
52	P	US 5331479 A	19940719		Capacitor assisted low voltage write circuit	360/68	360/46; 360/66		Madsen, Timothy A.	
53	P	US 5321559 A	19940614		Asynchronous peak detection of information embedded	360/46	360/51; 360/65		Nguyen, Hung C. et al.	
54	P	US 5279702 A	19940111		Data synchronizer with symmetric window generation	360/51	360/46; 375/376		Wilson, Ronald E. et al.	
55	P	US 5258933 A	19931102		Timing control for PRML class IV sampling data	708/3	360/46; 360/51		Johnson, Kenneth E. et al.	
56	P	US 5231545 A	19930727		Fault tolerant RLL data sector address mark decoder	360/49	341/59; 360/40		Gold, Clifford M.	
57	P	US 5212601 A	19930518		Disk drive data synchronizer with window shift synthesis	360/51	360/44		Wilson, Ronald E.	
58	P	US 5167615 A	19930216		Data separator and signal processing circuit	360/46	327/160; 360/51		Miyazawa, Shyoichi et al.	
59	P	US 5025327 A	19910618		Magnetic disk write precompensation method and	360/45	360/27		Stamness, Jesse I. et al.	
60	P	US 4989219 A	19910129		Midlevel carrier modulation and demodulation techniques	375/286	329/363; 332/115		Gerdes, Richard C. et al.	
61	P	US 4964107 A	19901016		Relative timing precompensation of	368/120	360/51		Galbraith, Richard L. et al.	
62	P	US 4878028 A	19891031		Technique for generating precompensation delay for	327/278	327/101; 327/262		Wang, Yun-Che et al.	
63	P	US 4809088 A	19890228		Integrated system for implementation of read	360/31	360/45; 360/51		Lofgren, Karl M. J. et al.	
64	P	US 4544964 A	19851001		Strobe for read/write chain	360/51			Burns, Kenneth R. et al.	
65	P	US 4281356 A	19810728		Magnetic disk memory	360/45	360/51		Sousa, Frank E.	

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